

SECRET

TID-266/67
1 November 1967

MEMORANDUM FOR: Assistant for Technical Development, NPIC

SUBJECT: Increased Funding on the [] Chip Comparators

25X1A

REFERENCE: Memo, your office, dated 20 October 1967 Subject as above

1. Currently, TID does not have [] in object class 31 FY 68 funds which it can commit to satisfy the improvements required to insure the reliable operation of the chip comparator indicated in the above reference. It would, however, provide adequate object class 25 FY 68 funds if approval is obtained from the Executive Director, NPIC.

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2. It is requested that every effort be made to purchase the modified chip comparator for TID. The reasons therefore are as follows:

a. A member of this division visited [] and is satisfied that with the modification it should provide TID with an advanced design precision measuring instrument capable of resolving 800 to 1200 line per millimeter at 200 magnification.

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b. The added cost (approximately 9% of the original investment) is considered necessary to insure the instruments reliability.

c. It will be used for production and for exhaustive testing and analysis, in conjunction with the point transfer and other inhouse devices, as a means to provide invaluable data, and possible software, for use with TID future acquisition, the Hi Precision Stereocomparator.

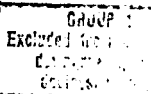
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Chief, Technical Intelligence Division

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DECLASS REVIEW by NIMA/DOD



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NPIC/TDS-201/67
1 November 1967

MEMORANDUM FOR THE RECORD

SUBJECT: Amplifications on [] Chip Comparator and Its
Operational Reliability

25X1A

REFERENCE: NPIC/TDS/D-1091-67 dated 24 October 1967, Subject: []
[] Chip Comparator Progress Report

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1. In relation to the above referenced memorandum, TDS would like to clarify the operational circumstances under which the comparator can be considered reliable. It is our belief that the basic technical difficulties with the comparators have been resolved based on the performance record of the prototype comparator now operating in IAS and the pre-delivery inspection of the two production comparators now under test at [].

2. At this time there is no reason to believe that these machines can not be successfully operated within the NPIC as long as the following services are provided:

a. That the comparators be installed in the proper environmental conditions as specified on the installation engineering form previously supplied the office of logistics. i.e. approximately 70°F ± 5°F and 55% R.H. ± 10% with reasonable air filtration. There is no reason to believe that building services cannot provide these conditions.

b. That IPD provide adequate software and computer time to meet operational requirements. Circumstances already permit on-line operation of the chip comparator presently in IAS, but the services are not as extensive as originally envisioned in the chip system concept. There is no reason that additional services cannot be provided by IPD, but it is up to the individual components to levy the requirements on IPD.

c. Operator training is a desirable additional feature that should be considered by components that do not have experienced

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25X1A

personnel in Photogrammetric Operations. In a personal sense anyone qualified to operate a [] Comparator can be thoroughly trained to operate a chip comparator in less than one hour.

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3. TDS feels that there are no foreseeable reasons why the chip comparators cannot be considered operationally acceptable in the near future, but since there is no individual with complete system responsibility it remains up to each component to request any necessary building services, computational and training support they may desire.

78

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[]
Assistant for Technical Development, NPIC

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